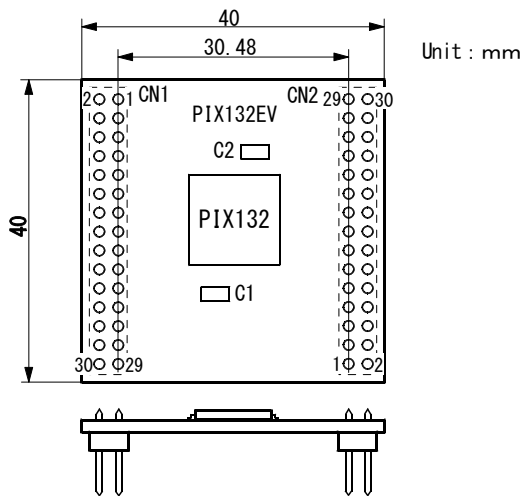




PIX132 is Evaluation Module on which PIX132 is mounted.
I/O signals of the IC are connected to the terminals
of the connectors (2 pcs.)

whose pins are 2.54mm pitch and 30 pins, and
the connectors are mounted on the rear surface of PCB.

■ Components

● PIX132		1
● Connectors	AXL230801 (Matsshta)	2
● Decoupling capacitor	0.1μF	2

■ CONNECTOR PIN ASSIGNMENT

C N 1		*1	
PIN NO.	SIGNAL	D	ICP#
1	GND *2		
2	VDD *2		
3	PA7	I/O	47
4	PA6	I/O	48
5	PA5	I/O	51
6	PA4	I/O	52
7	PA3	I/O	53
8	PA2	I/O	54
9	PA1	I/O	55
10	PA0	I/O	56
11	CLK	I	58
12	IMODEN	I	60
13	D7/PAHF3	I/O	61
14	D6/PAHF2	I/O	62
15	D5/PAHF1	I/O	63
16	D4/PAHF0	I/O	64
17	D3/PALF3	I/O	1
18	D2/PALF2	I/O	2
19	D1/PALF1	I/O	3
20	D0/PALF0	I/O	4
21	RESETN	I	6
22	CSN/PBF3	I	11
23	A2/PBF2	I	8
24	A1/PBF1	I	9
25	A0/PBF0	I	10
26	WRN/PAFE	I	12
27	RDN/PBFE	I	13
28	INTN/TEST	I/O	14
29	VDD *2		
30	GND *2		

C N 2		*1	
PIN NO.	SIGNAL	D	ICP#
1	GND *2		
2	VDD *2		
3	INSTB/HLDN	I	15
4	OTSTB/OEN	I/O	16
5	PD7	I/O	17
6	PD6	I/O	18
7	PD5	I/O	9
8	PD4	I/O	20
9	PD3	I/O	21
10	PD2	I/O	24
11	PD1	I/O	25
12	PD0	I/O	26
13	PC7	I/O	27
14	PC6	I/O	28
15	PC5	I/O	29
16	PC4	I/O	30
17	PC3	I/O	33
18	PC2	I/O	34
19	PC1	I/O	35
20	PC0	I/O	36
21	PB7	I/O	37
22	PB6	I/O	38
23	PB5	I/O	41
24	PB4	I/O	42
25	PB3	I/O	3
26	PB2	I/O	44
27	PB1	I/O	45
28	PB0	I/O	46
29	VDD *2		
30	GND *2		

Note :

*1: Column ICP# shows pin Pin no.
of PIX132 for each signal.

*2: GND and VDD pins are ground
and power supply pin. These pins
are connected to all VDD or GND
inside Module PCB.

Supply +5V or +3.3V to VDD.