NOVA electronics

2-Axis Motor Control IC with Interpolation Function [PbFree

CX312



MCX312 Functional Block Diagram



The block diagram of the whole function of MCX312

Block Diagram of the X and Y-axis Control Section



0.5

Accelerating speed

Speed (PPS)

30K

1K

+LIMITC

х

-300

+24V

triangle forms during S-curve acceleration/deceleration are prevented by a special method as the right figure however the number of output pulse is small.

■Automatic deceleration of non-symmetry trapezoidal drive Automatic deceleration can be operated on linear acceleration/deceleration drive whose accelerating and decelerating speed are different. It doesn't need for users to set the start point of deceletation by manual.

[Note] In case of acceleration>deceleration, there is a limiting point of the rate of deceleration/acceleration to execute decelerating automatically. Limitation depends on driving speed. For instance, when drive speed is 100kpps, its limitation is 1/40.

Built-in integral filter

Signals of limit and driving stop for each axis are photo coupler or CR integral filter is mounted on the circuit normaly. However MCX312 is equipped with integral type filters in the input stage of each input signal. It is possible to set a number of input signals whether the filter function is enabled or the signal is passed through. A filter time constant is selectable from eight stages.

Interpolation

2-axis linear interpolation

Linear interpolation is executed by writing linear interpolation command after setting speed parameter of X-axis and the point coordinates of X and Y to the present point coordinates. Linear interpolation moves from the present point coordinates to the finish point. Interpolation rang e of each axis is -8,388,607 ~ +8,388,607. Interpolation accuracy to the specified line is ± 0.5 LSB or less within the whole range. Interpolation drive speed is 1PPS ~ 4MPPS.

[Setting procedure for the operation of Fig.1]

1 Range XR= 8,000,000(Speed Multiple:1)

Fig.2 Example of CCW circular interpolation

2 Initial speed XSV= 1000

③ Drive speed
④ Finish point XV= 1000 (1000PPS) Drive speed

- XP= 500
- 5 Finish point YP=-300
- 6 2-axis linear interpolation drive

Circular interpolation

Circular interpolation is executed by writing CW or CCW circular interpolation command after setting the center and finish point coordinates to the present point (the start point). CW circular interpolation is starting from the present point to the finish point with clockwise direction, to the contrary, CCW circular interpolation drives to counterclockwise direction. The perfect circle appears by setting (0,0) to the finish point. The range of interpolation coordinates is -8,388,608 ~ +8,388,607 from the present point. The position tolerance for specified cicular curve is ± 1.88 within the whole interpolation range. Interpolation drive speed is 1PPS ~ 4MPPS(at CLK=16MHz).

[Setting procedure for the operation of Fig.2] [Setting procedure for the operation of Fig.3] 1 Range XR= 8,000,000(Speed multiple:1) (1) ~ (3) Same as Fig.2 2 Initial speed XSV= 500 (4) Center point XC= 5000 ③ Drive speed XV= 500(500PPS) (5) Center point YC = 0XP=0④ Center point XC= -10000 (6) Finish point ⑤ Center point YC= -10000 $\overline{\mathbf{7}}$ Finish point YP=06 Finish point XP= 0 (8) CW circular interpolation drive ⑦ Finish point YP= -20000 8 CCW circular interpolation drive Υ Y Start point (0,0) Center point (5000,0) Center point (-10000,-10000) Start point = Х Finish point (0,0) Finish point (0,-20000)



 $\overline{\eta}$

+5V

continuously. During the continuous interpolation, the driving will not stop; contrarily, the pulses are output continuously. When executing the continuous interpolation, the host CPU has to write the next interpolation segment into MCX312 before the previous interpolation segment is finished. Fig.4 shows the example of continuous interpolation from segment 1 to segment 8 of which start point is (0,0). In Segment 1,3,5 and 7, linear interpolation is executed. In segment 2,4,6 and 8, circular interpolation is executed of which tracks are quadrant circle with radius 1500.



Fig.4 Example of continuous interpolation

Multichip axes linear interpolation

This function is multichip axes linear interpolation using several chips of MCX312. Fig.5 shows the example of connecting for 4 axes linear interpolation using 2 pieces of MCX312. Transfer the data of interpolation via 8 signals for multichip interpolation(pin sharing with general input signal)



Fig.5 Example of connecting multichip axes interpolation



Fig.1 Example of 2-axis linear interpolation

Fig.3 Example of CW circular interpolation

Built-in filter

Continuous interpolation executes the sequence of interpolation drive

Time(SEC)

A = 116 D = 29

SV = 100 V = 3000

1.4 (SEC)

R = 800000(Multiple:10)

Ρ = 27500

Decelerating

D=36kpps/sec

Delay time of input signal

2 µ SEC

256 µ SEC

512 µ SEC

1.024mSEC

2.048mSEC

4.096mSEC

8.192mSEC

16.384mSEC

spee

2

0.6

FL2~0

0

5

6

Non-symmetry trapezoidal acceleration/deceleration drive(acceleration>deceleration)



MCX312

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Speed (PPS)

30K

1k

R = 800000(Multiple:10)

0

1.0(SEC)

Non-symmetry trapezoidal acceleration/deceleration drive(acceleration

■ Input/Output signals ((I): Input (O): Output (B): bidirectional Each X and Y axis has nOOOO signal. "n" means each X and Y axis.) CLK(I) Clock 16MHz(Standard) ●D15~0(B) Data Bus ●A3~0(I) Address ●CSN(I) Chip select ●WRN(I) Write strobe ●RDN(I) Read strobe ●RESETN(I) Reset ●H16L8(I) 16/8 Data bit bus width selection ●BUSYN(O) Executing the command ●INTN(O) Interrupt ●SCLK(O) 1/2CLK ●nPP/PLS(O) + direction drive pulse/Drive pulse ●nPM/DIR(O) - direction drive pulse/Direction ●nECA/PPIN(I) Encoder A-phase/Up pulse ●nECB/PMIN(I) Encoder B-phase/Down pulse ●nOUT7~0(O) General output (DSND: Decelerating, CNST: Constant speed driving, ASND: Accelerating, DRIVE: Outputting drive pulse, CMPM: P<COMP-, CMPP: P≥COMP+, ACDSND: Decreasing accelerating/decelerating speed, and ACASND: Increasing accelerating/decelerating speed, pin sharing with drive status output) ●nINPOS(I) In-position for servo driver ●nALARM(I) Servo driver alarm ●nLMTP(I) + direction limit ●nLMTM(I) - direction limit ●nSTOP2~0(I) 3points for decelrating/instant stop ●nEXPP(I) External + direction drive manual pulser A-phase ●nEXPM(I) External + direction drive, manual pulser B-phases ●EMGN(I) Emergency stop ●nINF5~0(I) foroints for general input/nIN5~2 are pin

drive, manual pulsar A-phase OnEXPM(I) External -direction drive, manual pulsar B-phase OEMGN(I) Emergency stop OnIN5~0(I) 6points for general input(nIN5~2 are pin sharing with multichip interpolation signal.)

Write register

| A0 | ddres A1 | ss A0 | Symbol | Register name | Contents | | | | |
|----|-------------|----------|---------------------|---|---|--|--|--|--|
| | | | | | Writing of the command in each axis and interpolation control section. | | | | |
| 0 | 0 | | | | <u>D15 D14 D13 D12 D11 D10 D9 D8 D7 D6 D5 D4 D3 D2 D1 D0</u> | | | | |
| | | 0 | WR0 | Command register | | | | | |
| | | | | | Axis assignment Command code | | | | |
| | | | | | ●D9~8 Axis assignment 0:non-select/1:select(Several axes selectable simultaneously) ●D15 1:Reset | | | | |
| 0 | 0 | 1 | XWR1 YWR1 | X axis mode register 1 Y axis mode register 1 | Setting of the logical levels and enable/disable of external decelerating/instant stop, interruption enable/disable for each axis and operation mode of the real position counter. <u>D15</u> D14 D13 D12 D11 D10 D9 D8 D7 D6 D5 D4 D3 D2 D1 D0 <u>D-ENDIC-STAIC-ENDIP2C+IP<c+ip<c+ip2c-ismodiepinviepclrisp2-eisp2-lisp1-eisp1-lisp0-eisp0-l< u=""> Interrupt enable/disable Drive decelerating/instant stop input signal enable/dis D5~0***-E 0:disable/1:enable ***-L Logical level 0:Low/1:Hi ●D6:Real position counter is cleared by STOP2 signal 0:disable/1:enable ●D7:Real position counter increase/decrease inversion function 0:disable/1:enable ●D8:Speed prior during S-curve acceleration/deceleration 0:disable/1:enable ●D15-9 Interrupt 0:disable/1:enable ●D9:Logical/real position counter COMP-variation ●D10:Logical/real position counter COMP-variation ●D12:Logical/real position counter</c+ip<c+ip2c-ismodiepinviepclrisp2-eisp2-lisp1-eisp1-lisp0-eisp0-l<></u> | | | | |
| | | | | | and the logical levels and enable/disable of servo motor signal for each axis. | | | | |
| | | | | X axis mode register 2 Y axis mode register 2 | D15 D14 D13 D12 D11 D10 D9 D8 D7 D6 D5 D4 D3 D2 D1 D0 INP-EINP-LIALM-EIALM-LIPIND1IPIND0IPINMDDIR-LIPLS-LIPLSMDICMPSLIHLMT-IHLMT+ILMTMDISLMT-ISLMT+ | | | | |
| 0 | 1 | 0 | XWR2 YWR2 | | ●D1,0 Software limit 0:disable/1:enable ●D2 Hardware limit 0:instant/1:decelerating stop ●D4,3 Logical level of limit signal 0:Low/1:Hi ●D5 COMP+/- register comparison 0:logical position counter/1:real position counter ●D6 Drive pulse outputting type 0:2-pulse system /1:1-pulse 1-direction system ●D7 Logicai level of drive pulse 0:positive logical pulse /1:negative logical pulse ●D8 Logical level of the direction signal 0:Low level for + direction/1:Hi for + direction ●D9 Encoder input signals 0:2-phase pulse/1:Up/Down pulse ●D11,10 Encoder input divide 00:1/1,01:1/2,10:1/4 ●D12 Logical level of ALARM signal 0:Low/1:Hi ●D13 ALARM signal 0:disable/1:enable ●D14 Logical level of INPOS signal 0:Low/1:Hi ●D15 INPOS signal 0:disable/1:enable | | | | |
| | | | BP1P | | Bit pattern interpolation X-axis + direction bit data | | | | |
| 0 | 1 | 1 | XWR3 YWR3 | X axis mode register 3 Y axis mode register 3 | Setting of the manual deceleration, symmetry/non-symmetry of acceleration/deceleration, external operation mode, general purpose output/drive status output switching and input signal filter. D15 D14 D13 D12 D11 D10 D9 D8 D7 D6 D5 D4 D3 D2 D1 D0 IFL2 FL1 FL0 FE4 FE3 FE2 FE1 FE0 IOUTSLIVRING IAVTRILEXOP1IEXOP0ISACCIDSNDEIMANLD Filter time constant Input signal filter enable/disable OD Deceleration of fixed pulse drive 0:automatic/1:manual OD1 Decelerating speed during deceleration 0:using value of accelerating speed(Symmetry)/1:using value of decelerating speed(non-symmetry) D2 Acceleration/deceleration mode 0:Trapezoidal/1:S-curve OD4,3 External driving operation 00:disable/01:continuous drive/10:fixed pulse drive/11:manual pulsar 0:disable/1:enable OD6 Position counter variable ring 0:disable/1:enable OD7 nOUT7~0 Output pin selecting 0:outputting OUT7~0 /1:outputting drive status(DSND,CNST,ASND,DRIVE,CMPM,CMPP,ACDSND and ACASND) OB EMG,LMTP/M,STOP0,1 signal filter 0:disable/1:enable OD9 STOP2 signal filter 0:disable/1:enable OD10 INPOS and ALARM signal filter 0:disable/1:enable OD11 EXPP/M signal filter 0:disable/1:enable OD12 IN5~0 signal filter 0:disable/ 1:enable OD5-D13 Setting of input filter time constant(000:0.002msec/ 001:0.2msec/ 010:0.5msec/ 011:1msec/ 100:2msec/ 101:4msec/ 111:6msec) | | | | |
| | | | BP1M | | Bit pattern interpolation X-axis -direction bit data. | | | | |
| 1 | 0 | 0 | WR4 Output register | Setting of general output signal nOUT7~0. 0:Low/ 1:Hi D15 D14 D12 D11 D10 D9 D8 D7 D6 D5 D4 D3 D2 D1 D0 YOUT7IYOUT6IYOUT5IYOUT4IYOUT3IYOUT2IYOUT1IYOUT0IXOUT7IXOUT6KOUT5IXOUT4XOUT3KOUT2KOUT1KOUT0 D1 D0 D0 | | | | | |
| 1 | | | BP2P | | Bit pattern interpolation Y-axis +direction bit data. | | | | |
| 1 | 0 | 1 | WR5 | Interpolation mode register | Setting of constant vector speed mode to execute interpolation drive, single step mode and interrupt in interpolation. D15 D14 D13 D12 D11 D10 D9 D8 D7 D6 D5 D4 D3 D2 D1 D0 BPINTICIINTICMPLSIEXPLSIMULTIIMULTOILSPD1LSPD0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 | | | | |
| | | | BP2M | | Bit pattern interpolation Y-axis -direction bit data. | | | | |
| 1 | 1 | 0 | WR6 | Write data register 1 | Setting of the low word 16 bit data for data writing. (D15~D0) | | | | |
| 1 | 1 | 1 | WR7 | Write data register 2 | Setting of the high word 16 bit data for data writing. (D31~D16) | | | | |
| L | | | | 5 - 1 | 5 5 ····· | | | | |

• The above table shows the address for 16-bit data bus. In 8-bit data bus access, using the address data for A3~A0, the 16-bit data bus are divided into the high word byte (D15~8) and the low word byte (D7~0). • Each X and Y axis has WR1,WR2 and WR3 (mode register 1, 2 and 3). Writing the data in these registers by the same adrress. It depends on the axis assignment of the last command to write the data in the mode register of which axis. Or, user can select the axis by writing the NOP command which is assigned an axis just before. ●BP1~2P and BP1~2M for bit pattern interpolation can not be written just after resetting. It is resolved by operating BP register data writing enabling (36h). At resetting, all the bits of nWR1, nWR2, nWR3, WR4 and WR5 registers are cleared to 0(n=X and Y). The other registers are undetermined.

Read register

| Address | | Symbol | Register name | Contents | | | |
|---|---|--------|---------------|--|--|--|--|
| Az AT Au August Manual Displaying drive and error status of each axis. Displaying interpolation D15 D14 D13 D15 D14 D15 D16 D15 D14 D17 D14 D15 | | | | Main status register | Displaying drive and error status of each axis. Displaying interpolation driving status. D15 D14 D12 D11 D10 D9 D8 D7 D6 D5 D4 D3 D2 D1 D0 - IBPSC0IZONE2IZONE1IZONE0ICNEXTII-DRV - - IY-ERRIX-ERR - IY-DRVIX-DRV | | |
| | | | | Indin Status register | error of each axis drive of each axis ●D1,0 1:driving ●D5,4 1:error occuring(become "1" whichever from RR2/D6~0, RR1/D15~12.) ●D8 1:interpolation driving ●D9 1:writable the next data of continuous interpolation ●D12~10 circular interpolation quadrant 000:0, 001:1, 010:2, …111:7 ●D14, 13 bit pattern interpolation stack counter 00:0,011:1,102,111:3 | | |
| 0 | 0 | 1 | XRR1 YRR1 | X-axis status register 1 Y-axis status register 1 | Displaying the comparison of positoin counter and COMP± register, status of aceeleration/deceleration during the driving and driving termination status. D15 D14 D13 D12 D11 D10 D9 D8 D7 D6 D5 D4 D3 D2 D1 D0 [EMG[ALARM]LMT-]LMT+] - ISTOP2[STOP1]STOP0[ADSND]ACNST[AASND]DSND]CNST[ASND]CMP-]CMP+] status of driving termination ●D0 1:position counter2COMP+ ●D1 1:position counter <comp- 1:accelerating="" 1:constant="" driving<br="" speed="" ●d2="" ●d3="">●D4 1:decelerating ●D5 1:increasing accelerating/decelerating speed ●D6 1:constant speed of accelerating/decelerating ●D7 1 decreasing accelerating/decelerating speed ●D15-8 1:factor of driving termination</comp-> | | |
| 0 | 1 | 0 | XRR2 YRR2 | X-axis status register 2 Y-axis status register 2 | Displaying the error information. D15 D14 D13 D12 D11 D10 D9 D8 D7 D6 D5 D4 D3 D2 D1 D0 - - - - - - - - MULTIEMGIALARM HLMT-IHLMT+ISLMT-ISLMT+ ●D0 1:+direction software limit ●D1 1:-direction software limit ●D2 1:+direction limit signal on ●D3 1:-direction limit signal on ●D3 1:-direction limit signal on ●D4 signal on ●D4 1:alarm signal on for servo motor ●D5 1:emergency stop signal on ●D6 1:error occuring in sub chip at multichip interpolation driving | | |
| 0 | 1 | 1 | XRR3 YRR3 | X-axis status register 3 Y-axis status register 3 | Displaying the factor of interrupt occuring (interpolation excluded). D15 D14 D13 D12 D11 D10 D9 D8 D7 D6 D5 D4 D3 D2 D1 D0 - - - - - D-ENDIC-STAIC-ENDIP≥C+IP <c+ip<c-ip≥c-i< td=""> - 1:interrupt occuring Each bit for D7~D1 is corresponding to D15~D9 of WR1(mode register1).</c+ip<c-ip≥c-i<> | | |
| 1 | 0 | 0 | RR4 | Input register 1 | Displaying the status of X-axis input signal. 0:Low 1:Hi D15 D14 D13 D12 D11 D10 D9 D8 D7 D6 D5 D4 D3 D2 D1 D0 X-LM-IX-LM+IX-IN5[X-IN4] X-IN2[X-IN1]X-IN0[X-ALM] X-INP[X-EX-IX-EX+] EMG[X-ST2]X-ST1] X-ST0 | | |
| 1 | 0 | 1 | RR5 | Input register 2 | Displaying the status of Y-axis input signal. 0:Low 1:Hi D15 D14 D12 D11 D10 D9 D8 D7 D6 D5 D4 D3 D2 D1 D0 Y-LM-IY-LM+IY-IN5 Y-IN3 Y-IN2 Y-IN1 Y-IN0 Y-ALM Y-INP Y-EX-IY-EX+I - Y-ST2 Y-ST1 Y-ST0 | | |
| 1 | 1 | 0 | RR6 | Read data register 1 | Setting of the low word 16 bit data for data reading. (D15~D0) | | |
| 1 | 1 | 1 | RR7 | Read data register 2 | Setting of the high word 16 bit data for data reading. (D31~D16) | | |

• The above table shows the address for 16-bit data bus. In 8-bit data bus access, the 16bit data bus are divided into the high word byte (D15~8) and the low word byte(D 7~0) using A3~A0 address signal. ● Each X and Y-axis has RR1, RR2 and RR3 (status register 1,2 and 3). Data can be read in these registers by the same address. It depends on the axis assignment of the last command to write the data in the mode register of which axis. Or, user can select the axis by writing the NOP command which is assigned an axis just before.

Data writing commnads

| Code | Setting Command | Symbol | Data range | Data length (byte) |
|------|-----------------------------|--------|---------------------------------------|-----------------------|
| 00 | Range | R | R8,000,000(multiple=1) ~ 16,000(=500) | 4 bytes |
| 01 | Jerk | K | 1 ~ 65,535 | 2 |
| 02 | Acceleration | A | 1 ~ 8,000 | 2 |
| 03 | Deceleration | D | 1 ~ 8,000 | 2 |
| 04 | Initial speed | SV | 1 ~ 8,000 | 2 |
| 05 | Drive speed | V | 1 ~ 8,000 | 2 |
| 06 | Output pulse numbers | P | Output pulse numbers:0 ~ 268,435,455 | 4 |
| | Interpolation finish point | | Finish point:-8,388,608 ~ +8,388,607 | 4 |
| 07 | Manual deceleration point | DP | 0 ~ 268,435,455 | 4 |
| 08 | Center point of circle | С | -8,388,608 ~ +8,388,607 | 4 |
| 09 | Logical position counter | LP | -2,147,483,648 ~ +2,147,483,647 | 4 |
| 0A | Real position counter | EP | -2,147,483,648 ~ +2,147,483,647 | 4 |
| 0B | COMP+ register | CP | -1,073,741,824 ~ +1,073,741,823 | 4 |
| 0C | COMP- register | CM | -1,073,741,824 ~ +1,073,741,823 | 4 |
| 0D | Acceleration counter offset | AO | -32,768 ~ +32,767 | 2 |
| 0F | NOP(for switching) | | | |

Data reading commands

| Code | Reading Command | Symbol | Data range | Data length (byte) |
|------|--|--------|-------------------------------|-----------------------|
| 10 | Logical position counter | LP | -2,147,483,648~+2,147,483,647 | 4 bytes |
| 11 | Real position counter | EP | -2,147,483,648~+2,147,483,647 | 4 |
| 12 | Current drive speed | CV | 1 ~ 8,000 | 2 |
| 13 | Acceleration / deceleration | CA | 1 ~ 8,000 | 2 |
| 14 | Maximum finish point reading for multichip linear interpolation | MX | 0 ~ 8,388,607 | 4 |

■ Interpolation commands

Driving commands

| Code | Commands |
|------|--|
| 30 | 2-axis linear interpolation |
| 31 | |
| 32 | CW circular interpolation |
| 33 | CCW circular interpolation |
| 34 | 2-axis BP interpolation |
| 35 | |
| 36 | BP register writable |
| 37 | BP register unwritable |
| 38 | BP data stack |
| 39 | BP data clear |
| ЗA | single step interpolation |
| 3B | Deceleration enable |
| 3C | Deceleration disable |
| 3D | Interpolation interrupt clear |
| 3E | Maximum finish point clear for multichip linear interpolation |

23 24

- -direction continuous drive drive start holding drive start holding release 25
- /termination status clear 26 decelerating stop

Commands

+direction fixed pulse drive

-direction fixed pulse drive

+direction continuous drive

instant stop 27

Parameter caluculation at CLK= 16MHz

Code

20

21

22

Initial speed(PPS)= SV × M Drive speed(PPS)= V × M

The Specifications are subject to change without notice due to the technical development. 20194

Dsitributor



*BP= bit pattern

Multiple (M)= _____

Jerk (PPS/SEC²) = $\frac{62.5 \times 10^6}{\kappa} \times M$

Acceleration (PPS/SEC)= A × 125 × M Deceleration (PPS/SEC)= D × 125 × M

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