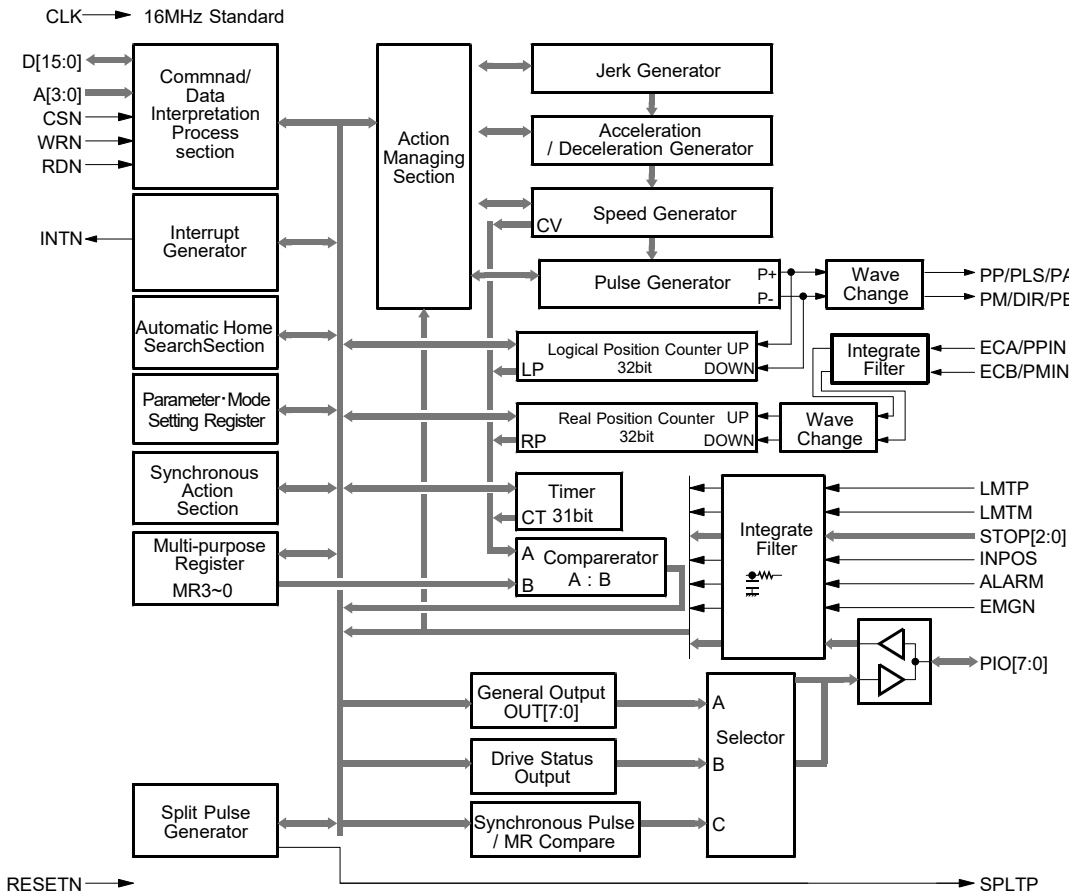


MCX501 is 1-axis motion control IC which can control either stepper motor driver or pulse type servo motor for position and speed control and perform trapezoidal/S-curve precise and smooth acceleration/deceleration drive.

This IC is epoch-making motion control IC which is equipped with speed range-free, timer and split pulse as new functions.

MCX501 Functional Block Diagram

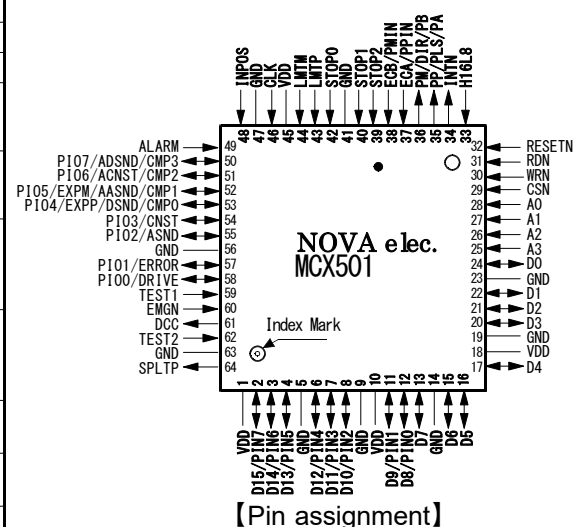


- 64pin plastic TQFP 0.5mm pin pitch
- Dimension(L×W×H) 10×10×1.0 mm
- RoHS compliant

Input/Output signal

Signal *1	Pin No.*2	Description
CLK(I)	46	Clock 16MHz(Standard)
D15/PIN7~D8/PIN0(B)	2~4,6~8, 11~12	Data Bus/Universal Input
D7~D0(B)	13,15~17, 20~22,24	Data Bus
A3~0(I)	25~28	Address
CNS(I)	29	Chip select
WRN(I)	30	Write strobe
RDN(I)	31	Read strobe
RESET(I)	32	Reset
H16L8(I)	33	16/8 Data bit bus width selection
TEST1/2(-)	59,62	Test
INTN(O)	34	Interrupt
PP /PLS /PA(O)	35	+ direction drive pulse /Drivepulse/A-phase signal
PM /DIR /PB(O)	36	- direction drive pulse /Direction/B-phase signal
ECA /PPIN(I)	37	Encoder A-phase /Up pulse
ECB /PMIN(I)	38	Encoder B-phase /Down pulse
STOP2~0(I)	39,40,42	Decelerating stop /Instant stop
LTMP(I)	43	+ direction limit

Signal *1	Pin No.*2	Description
LMTM(I)	44	- direction limit
INPOS(I)	48	In position
ALARM(I)	49	Servo alarm
PIO7 /ADSND /CMP3(B)	50	General input output 7 /Acceleration descend /Compare MR3
PIO6 /ACNST /CMP2(B)	51	Universal input output 6 /Acceleration constant /Compare MR2
PIO5 /EXPM /AASND /CMP1(B)	52	Universal input output 5 /External operation- /Acceleration ascend /Compare MR1
PIO4 /EXPP /DSND /CMP0(B)	53	Universal input output 4 /External operation+ /Descend/Compare MR0
PIO3 /CNST(B)	54	Universal input output 3 /Constant
PIO2 /ASND(B)	55	Universal input output 2 /Ascend
PIO1 /ERROR(B)	57	Universal input output 1 /Error
PIO0 /DRIVE(B)	58	Universal Input Output 0 /Drive
EMGN(I)	60	Emergency stop
DCC(O)	61	Deviation counter clear
SPLTP(O)	64	Split pulse



[Note]
 *1 Each I, O, and B means input, output and bi-directional.
 *2 These terminals are used commonly by multi-functions, no. 2~4, 6~8, 11, 12, 35~38, 50~55, 57, 58.

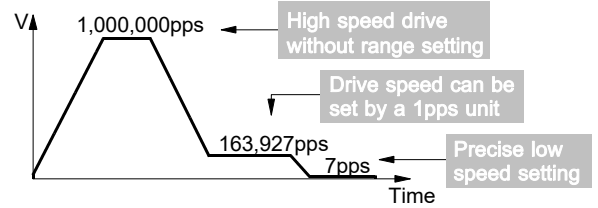
Speed range free

MCX501 can set the drive speed by a 1pps unit from 1pps to 8Mpps without range setting (range-free).

When setting the drive speed by using Multiple(Range setting),

- Setting Multiple small for precise speed setting at low speed
→ It can not change to high speed drive.
- Setting Multiple large for high speed drive
→ It can not set precise drive speed.

There were the above limitations for the previous ICs. By speed range free, such inconvenience has been taken away and MCX501 can change the speed directly from low speed, 1pps, 2pps to high speed pulse like 1Mpps during driving.



Acceleration/deceleration driving

◆ Acceleration/deceleration drive

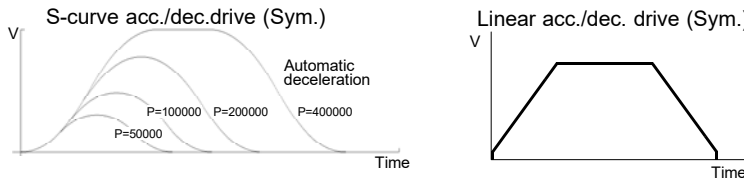
MCX501 can perform multiple acceleration/deceleration driving, constant speed drive, trapezoidal acceleration/deceleration drive(symmetrical/non-symmetrical), S-curve acceleration/deceleration drive(symmetrical/non-symmetrical).

◆ Automatic deceleration

In position drive of trapezoidal acceleration/deceleration (symmetrical/non-symmetrical), S-curve acceleration/deceleration (symmetrical only), MCX501 calculates the deceleration start point and starts deceleration drive automatically.

◆ S-curve acceleration/deceleration

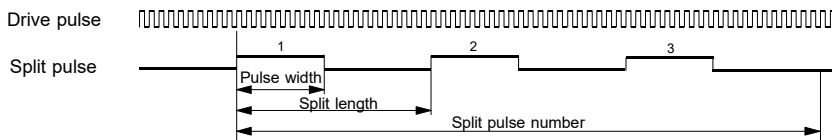
In S-curve acceleration/deceleration, a primary linear increase/decrease method is applied. Therefore the speed acceleration/deceleration profile forms parabolic S-curve. Triangle waveforms during S-curve acceleration/deceleration are prevented by a special method.



*Non-symmetrical acc/dcc are also possible.

Split pulse

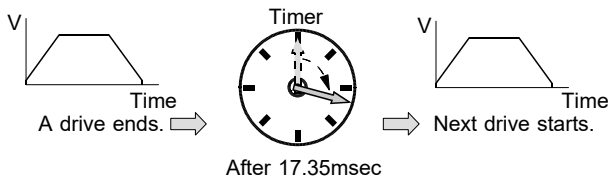
MCX501 can output split pulses during driving. Synchronizing an axis's movement, various kinds of actions can be performed in specified intervals. Split length, pulse width and pulse numbers can be set. By combining the split pulse output and the synchronous action, start/stop split pulse from a specified position and split length and pulse width can be changed by external signal input.



Built-in timer

MCX501 is equipped timer. It can be set by a 1μsec unit 1 ~ 2,147,483,647μsec time range(at CLK=16MHz).

It can be performed the various actions precisely as follows by combining the timer and the synchronous action.

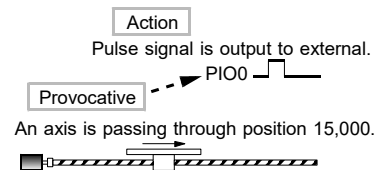


- After the driving is finished, start the driving after waiting a specified time.
- After the external signal is input, start the driving after waiting a specified time.
- Stop the continuous driving after waiting a specified time.
- Measuring the passing time from position A to B. and so on.

Synchronous Actions

The synchronous action is a function that performs a specified action when a specified activation factor (provocative) occurs the axis or with a device outside of IC by linking with a provocative.

The specified action by the synchronous action is very fast and precise because there is not any intervention of CPU. It can be set 4 sets as synchronous action. 1set of synchronous action consists of a specified provocative and a specified action. 15 types of provocatives are available, passing the specified position, starting/stopping of driving, rising/falling of an external input signal, when the timer is finished and so on. 28 types of actions are available, start/stopping of driving, saving the value of the current position counter in the multi-purpose register, writing the drive speed and so on. More various applications can be performed by combining the plural synchronous actions. Also, repeat of action can be settable.



Commands

■ Commands for data writing

Code	Commands	Symbol	Data range	Data length (byte)
00h	Jerk setting	JK	1 ~ 1,073,741,823 [pps/sec ²]	4
01	Deceleration increasing rate setting	DJ	1 ~ 1,073,741,823 [pps/sec ²]	4
02	Acceleration setting	AC	1 ~ 536,870,911 [pps/sec]	4
03	Deceleration setting	DC	1 ~ 536,870,911 [pps/sec]	4
04	Initial speed setting	SV	1 ~ 8,000,000 [pps]	4
05	Drive speed setting	DV	1 ~ 8,000,000 [pps]	4
06	Drive pulse number /Finish point setting	TP	-2,147,483,646 ~ +2,147,483,646	4
07	Manual deceleration point setting	DP	0 ~ 4,294,967,292	4
09	Logical position counter setting	LP	-2,147,483,648 ~ +2,147,483,647	4
0A	Real position counter setting	RP	-2,147,483,648 ~ +2,147,483,647	4
0B	Software limit + setting	SP	-2,147,483,647 ~ +2,147,483,647	4
0C	Software limit - setting	SM	-2,147,483,647 ~ +2,147,483,647	4
0D	Acceleration counter offsetting	AO	-32,768 ~ +32,767	2
0E	Logical position counter maximum value setting	LX	1 ~ 2,147,483,647(7FFF FFFFh) or FFFF FFFFh	4
0F	Real position counter maximum value setting	RX	1 ~ 2,147,483,647(FFF FFFFh) or FFFF FFFFh	4
10	Multi-purpose register 0 setting	MR0	-2,147,483,648 ~ +2,147,483,647	4
11	Multi-purpose register 1 setting	MR1	-2,147,483,648 ~ +2,147,483,647	4
12	Multi-purpose register 2 setting	MR2	-2,147,483,648 ~ +2,147,483,647	4
13	Multi-purpose register 3 setting	MR3	-2,147,483,648 ~ +2,147,483,647	4
14	Home detection speed setting	HV	1 ~ 8,000,000 [pps]	4
15	Speed increasing / decreasing value setting	IV	1 ~ 1,000,000 [pps]	4
16	Timer value setting	TM	1 ~ 2,147,483,647 [μ sec]	4
17	Split pulse setting 1	SP1	Split length: 2 ~ 65,535 Pulse width: 1 ~ 65,534	4
18	Split pulse setting 2	SP2	Split pulse number: 0 ~ 65,535	2

■ Commands for data reading

Code	Commands	Symbol	Data range	Data length (byte)
30h	Logical position counter reading	LP	-2,147,483,648 ~ +2,147,483,647	4
31	Real position counter reading	RP	-2,147,483,648 ~ +2,147,483,647	4
32	Current drive speed reading	CV	0 ~ 8,000,000 [pps]	4
32	Current acceleration/deceleration reading	CA	0 ~ 536,870,911 [pps/sec]	4
34	Multi-purpose register 0 reading	MR0	-2,147,483,648 ~ +2,147,483,647	4
35	Multi-purpose register 1 reading	MR1	-2,147,483,648 ~ +2,147,483,647	4
36	Multi-purpose register 2 reading	MR2	-2,147,483,648 ~ +2,147,483,647	4
37	Multi-purpose register 3 reading	MR3	-2,147,483,648 ~ +2,147,483,647	4
38	Current timer value reading	CT	0 ~ 2,147,483,647 [μ sec]	4
3D	WR1 setting value reading	WR1	(Bit data)	2
3E	WR2 setting value reading	WR2	(Bit data)	2
3F	WR3 setting value reading	WR3	(Bit data)	2
40	Multi-purpose register mode setting reading	MRM	(Bit data)	2
41	PIO signal setting 1 reading	P1M	(Bit data)	2
42	PIO signal setting 2 and other settings	P2M	(Bit data)	2
43	Acceleration setting value reading	AC	1 ~ 536,870,911 [pps/sec]	4
44	Initial speed setting value reading	SV	1 ~ 8,000,000 [pps]	4
45	Drive speed setting value reading	DV	1 ~ 8,000,000 [pps]	4
46	Drive pulse number/finish point setting value reading	TP	-2,147,483,646 ~ +2,147,483,646	4
47	Split pulse setting 1 reading	SP1	Split length : 2 ~ 65,535 Pulse width : 1 ~ 65,534	4

【Note】

- Unit of speed parameter value and timer value is used when CLK is 16MHz.

■ Mode writing command

Code	Commands	Symbol	Data length (byte)
20h	Multi-purpose register mode setting	MRM	2
21	PIO signal setting	P1M	2
22	PIO signal setting 2 and other settings	P2M	2
23	Automatic home search mode setting 1	H1M	2
24	Automatic home search mode setting 1	H2M	2
25	Input signal filter mode setting	FLM	2
26	Synchronous action SYNC 0 setting	S0M	2
27	Synchronous action SYNC 1 setting	S1M	2
28	Synchronous action SYNC 2 setting	S2M	2
29	Synchronous action SYNC 3 setting	S3M	2

■ Drive commands

Code	Commands
50h	Relative position drive
51	Counter relative position drive
52	+ direction continuous pulse drive
53	- direction continuous pulse drive
54	Absolute position drive
56	Drive decelerating stop
57	Drive instant stop
58	+ setting for direction signal
59	- setting direction signal
5A	Automatic home search execution

■ Synchronous action operating commands

Code	Commands
81~8Fh	Synchronous action enable setting
91~9F	Synchronous action disable setting
A1~AF	Synchronous action activation

■ Other settings

Code	Commands
70h	Speed increasing
71	Speed decreasing
72	Deviation counter clear output
73	Timer start
74	Timer stop
75	Split pulse start
76	Split pulse stop
79	Error finish status clear
1F	NOP
FF	Command reset

Other Specifications

- Control axis 1-axis
- Data bus 16/8 bit selectable
- Drive pulse output pulse (When CLK is 16MHz.)
 - Drive speed range 1pps ~ 8Mpps (When CLK=20MHz: up to 10Mpps)
 - Initial speed 1pps ~ 8Mpps (When CLK=20MHz: up to 10Mpps)
 - Output speed accuracy Within $\pm 0.1\%$ (According to the setting value.)
 - Acceleration Range 1 pps/sec ~ 536,870,911 pps/sec
 - Acceleration Increasing/ Decreasing Rate Range *1
1 pps/sec² ~ 1,073,741,823 pps/sec²
 - Acceleration/Deceleration Curve
Constant speed, Symmetrical/non-symmetrical linear acceleration/
deceleration, Symmetrical/non-symmetrical parabola S-curve acceleration/
deceleration
 - Drive Pulse Range *2
 - Relative position driving : -2,147,483,646 ~ 2,147,483,646
 - Absolute position driving : -2,147,483,646 ~ 2,147,483,646
 - Position drive decelerating stop mode Automatic/Manual *3
 - Override
Output pulse number and drive speed during driving are changeable. *4
 - Drive commands
Relative position drive, Absolute position drive, +/- direction continuous
drive
 - Triangle form prevention
For both trapezoidal and S-curve acceleration/deceleration.
 - Type of drive pulse output
Independent 2-pulse, 1-pulse 1direction, 2 phase double and quad count edge
evaluation are selectable.
 - Drive pulse output logic
Positive logic or negative logic is selectable.
 - Drive pulse output terminal Terminals can be replaced.
- Encode input pulse
 - Input pulse type
2-phase single, double, quad count evaluation edge, up/down pulse are
selectable.
 - Input pulse terminal Terminals can be replaced.
- Position counter *5
 - Logical position counter count range -2,147,483,648 ~ +2,147,483,647
 - Real position counter count range -2,147,483,648 ~ +2,147,483,647
 - Variable ring Possible to set the count maximum value of each counter.
- Software limit
 - Setting range -2,147,483,648 ~ +2,147,483,647
 - Stop mode Decelerating/instant stop is selectable.
- Multi purpose register
 - Size·number 32-bit·4 pcs
 - Usage Comparison of position, speed and timer. Register of position and
speed. Save the current position, speed and the value of timer.
- Split pulse
 - Split Length 2 ~ 65,535 pulses *6
 - Split Pulse Width 1 ~ 65,534 pulses
 - Split Pulse Number 1 ~ 65,535, or up to infinity
- Automatic home search
 - Sequence
STEP1 High-speed home search → STEP2 Low-speed home search
→ STEP3 Encoder Z-phase search → STEP4 Offset driving
· Enable/disable and search signal and direction for each step are selectable.
 - Deviation counter clear output
Clear pulse width from range of 10 μ ~ 20msec and logical level are selectable.
 - Timer between steps
Selectable from the range of 1msec ~ 1,000msec.
- Interrupt
 - Interrupt factor
When comparison of multi-purpose registers are changed, when drive starts/
stops, acceleration/deceleration drive starts/stops at constant speed area and
so on.
 - Enable/disable Valid/invalid for each interrupt factor is selectable.
- Timer
 - Setting Range 1 ~ 2,147,483,647 μ sec
- Synchronous action
 - Number of sets 4sets *7
 - Provocative
When the comparison of multi-purpose register changes, activation
command, when timer expires and so on.
 - Action Load value (MRn→setting value), save value (MRn←current
value), synchronous pulse output to the external and so on.
- External signal for driving *8
 - EXPP, EXPM signals for relative position drive and continuous drive.
 - Driving in manual pulsar mode(encoder input : quadrature single count
edge evaluation)
- External stop signal
 - Number of signal 3 points (STOP0~2)
 - Enable/disable
Enable/disable for stop signal function is selectable. *9
 - Logical level
Low active/Hi active is selectable.
 - Stop mode
When active, drive decelerating stop. (When lower than initial speed,
driving stops immediately)
- Input signal for servo motor
 - Signal types ALARM (Alarm), INPOS (In-position check),
DCC (Deviation counter clear)
 - Enable/disable Enable/disable of signal is selectable.
 - Logical level Low active/Hi active is selectable
- General output/input signal
 - Number of signal 8 points
 - Synchronous input, pins share the input pin for driving by external signals.
 - Synchronous action output, multi-purpose register comparison output,
pins share drive status output signal pins.
- Drive status signal output
 - Signals *10
· Driving, error occurring, accelerating/constant speed driving/decelerating,
acceleration increasing/constant/decreasing.
· Drive status can be readable by status register.
- Over run limit signal input
 - Number of signal 2 points (each + direction, - direction.)
 - Enable/disable Enable/disable for limit function is selectable. *9
 - Logical level Low active/Hi active is selectable.
 - Stop mode When active, instant/decelerating stop is selectable.
 - Input pulse terminal Terminals are changeable.
- Emergency stop signal
 - EMGN 1 point In Low level, stop drive pulse output. (Logical level
can not be set.)
- Integral filter built-in
 - Input signal filter Equip integral filter in input column of each signal.
 - Filter time constant
Selectable from 16types (500n,1 μ ,2 μ ,4 μ ,8 μ ,16 μ ,32 μ ,64 μ ,128 μ ,256 μ ,
512 μ ,1m,2 m,4 m,8 m,16 m[sec])
 - Enable/disable Enable/disable of filter function is selectable.
- Electrical characteristics
 - Temperature range for operation -40°C ~ +85°C
 - Power voltage +3.3V $\pm 10\%$
 - Consumption current 27mA(Average), 44mA(Max) at CLK=16MHz
 - Clock 16MHz(Standard), 20MHz(Max)
 - Input signal level TTL level (5V tolerant)
 - Output signal level
3.3V CMOS level (Only TTL can be connect to 5V type.)
- Package
 - 64-pin plastic TQFP, pin pitch : 0.5mm, RoHS compliant
 - Dimension : 10 × 10 × 1.0 mm

<Remarks>

- *1 Parameter that is used in S-curve acceleration/deceleration driving.
- *2 Pulse range that can be set for the driving that outputs specified pulses. In continuous pulse driving, pulses are output up to infinity
- *3 Automatic decelerating stop is performed by calculating a start point of decelerating according to a specified moving pulse value inside IC. Manual decelerating stop is performed by upper CPU to decide a start point of decelerating. MCX501 can perform automatic decelerating stop except non-symmetrical S-curve acceleration/deceleration.
- *4 After the start of driving, output pulse number can be changed for the same direction in only relative position driving.
- *5 Logical position counter counts output pulses and real position counter counts encoder input pulses.
- *6 While driving, split pulses are output at specified intervals in synchronization with driving pulses.
- *7 1 set of synchronous actions is configured with one specified activation factor and one specified action.
- *8 Input pins for external signals share the general purpose input/output pins.
- *9 When the function is not used, it can be used as general purpose input.
- *10 Drive status output signal pins share the general purpose input/output pins.

The Specifications are subject to change without notice due to the technical improvement.
2019.4

Distributor

NOVA electronics, Inc.
4F Belle plaza II, 3-2-15 Sasazuka, Shibuya-Ku, Tokyo 151-0073, Japan
WEB SITE <http://www.novaelec.co.jp/eng>
EMAIL ADDRESS novaelec_info@novaelec.co.jp
TEL 81-3-6300-0615 FAX 81-3-6300-0617